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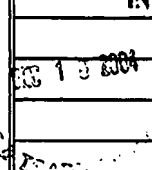
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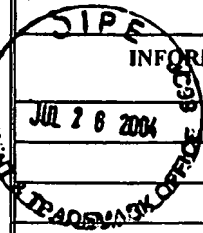
PTO/SB/08B (Substitute for form 1449/PTO)		Attorney Docket No.: 004-30132	
		Application No.: 10/789,311	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: Sheueling Chang Shantz	
(Use several sheets if necessary)		Filing Date: February 27, 2004	
		Group Art Unit: 2124	
		Examiner Name: Not yet assigned	
		Date Submitted: December 7, 2004	
NON PATENT LITERATURE DOCUMENTS			
Examiner Initial*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/CJ/		Yvonne Hitchcock, et al., "Implementing an efficient elliptic curve cryptosystem over GF(p) on a smart card," ANZIAM J. 44(E), April 2003, pp. C354-C377.	
/CJ/		National Institute of Standards and Technology, "Recommended Elliptic Curves for Federal Government Use," August 1999, 43 pages.	
/CJ/		Hasegawa, Toshio, et al., "A Practical Implementation of Elliptic Curve Cryptosystems over GF(p) on a 16-bit Microcomputer," In <i>Public Key Cryptography PKC '98</i> , vol. 1431 of <i>Lecture Notes in Computer Science</i> , pages 182-194.	
Examiner Signature	/Carlton Johnson/	Date Considered	06/09/2007

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(Use several sheets if necessary)		Filing Date: February 27, 2004
		Group Art Unit: 2124
		Date Submitted: July 21, 2004
NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
/CJ/	AA	Intel® Itanium™ Processor, "High Performance On Security Algorithms (RSA Decryption Kernel)," Intel Corporation, 2001, pp. 1-8.
	AB	Intel®, "Intel® Itanium™ Architecture Software Developer's Manual, Volume 1: Application Architecture," Revision 2.1, October, 2002, 2 pages.
	AC	Cohn, Leonard Allen, "Generate-Propagate Adders," ChoPP Computer Corporation, prior to 2000, pp. 1-16.
	AD	Fairchild, "F100K ECL Data Book," Fairchild Camera and Instrument Corporation, 1982, pp. 3-177 - 3-178.
	AE	Großschädl, Johann, "Instruction Set Extension for Long Integer Modulo Arithmetic on RISC-Based Smart Cards," Proceedings of the 14 th Symposium on Computer Architecture and High Performance Computing, 2002, 7 pages.
	AF	Koç, Cetin Kaya, "High-Speed RSA Implementation," Version 2.0, RSA Laboratories, November, 1994, pp. i-70.
	AG	Mano, M. Morris, "Computer System Architecture," Prentice-Hall, Inc., 1976, pp. 244-249.
	AH	Shantz, Sheueling Chang, "From Euclid's GCD to Montgomery Multiplication to the Great Divide," Sun Microsystems, June 2001, pp. 1-10.
	AI	Standards for Efficient Cryptography, "SEC 2: Recommended Elliptic Curve Domain Parameters," Certicom Research, September 20, 2000, pp. i-45.
	AJ	Woodbury, A.D.; Bailey, Daniel V., Paar, Christof, "Elliptic Curve Cryptography on Smart Cards Without Coprocessors," The Fourth Smart Card Research and Advanced Applications (CARDIS2000) Conference, Bristok, UK, pp. 71-92.
	AK	H. Cohen, A. Miyaji, and T. Ono, "Efficient elliptic curve exponentiation using mixed coordinates", in K. Ohta and D. Pei, editors, Advances in Cryptology ASIACRYPT 98, pp. 51-65, Springer Verlag, 1998, LNCS 1514
	AL	D. Bailey and C. Paar, "Optimal Extension Fields for Fast Arithmetic in Public-Key Algorithms." In H. Krawczyk, editor, Advances in Cryptography - CRYPTO '98, volume LNCS 1462, pages 472-485. Springer-Verlag, 1998. http://citeseer.ist.psu.edu/article/bailey98optimal.html , 14 pages.
	AM	H. Pietiläinen, "Elliptic Curve Cryptography on Smart Cards," Master's Thesis, Helsinki University of Technology, Oct. 12, 2000, pp. i-81.
↓	AN	F. Morain and J. Olivos, "Speeding Up the Computations on an Elliptic Curve Using Addition-Subtraction Chains," Rapport de Recherche 983, INRIA, France, March 1989, http://citeseer.ist.psu.edu/morain90speeding.html , pp. 119-130.
	AO	
	AP	
	AQ	
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